5.

(a) RGB =

Extract out

(b) <https://courses.cs.washington.edu/courses/cse378/97au/help/compilation.html>

(c) load-store architecture. Load value into register first, then manipulate using that register, then save back the value using the register

6.

(a) i) Single core processes can only execute one process at a time

ii) Memory can store many processes

(b) Biggest to smallest memory

(c)

A close up of text on a whiteboard

Description automatically generated